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09/927861



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Franke

Serial No. : t/b/a Examiner: t/b/a

Filed : August 10, 2001 Group Art Unit: t/b/a

For : METHOD AND COMPUTER SYSTEM FOR CARRYING OUT  
SIMULATION

## INFORMATION DISCLOSURE STATEMENT

I hereby certify that this paper is being deposited with the United States  
Postal Service as first class mail in an envelope addressed to:  
Commissioner for Patents, Washington, D.C. 20231

August 10, 2001

Date of Deposit

Paul D. Ackerman

Attorney Name

Signature

39,891

PTO Registration No.

August 10, 2001

Date of Signature

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to the provisions of 37 C.F.R. §§ 1.97 and 1.98, Applicant  
respectfully requests that the references relating to the above-mentioned application listed  
herein be made of record in the U.S. Patent and Trademark office. The referenced  
citations are listed in the accompanying PTO Form 1449. Copies of these references are  
also enclosed.

No fee is believed to be required in connection with this communication.

However, if a fee is required, the Commissioner is hereby authorized to charge the payment of any fees fees to Deposit Account No. 02-4377.

Respectfully submitted,



Paul D. Ackerman  
Patent Office Reg. No. 39,891

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Enclosures

<b>Form PTO-1449 U.S. Department of Commerce (REV. 2-82) Patent and Trademark Office</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	Atty. Docket No. 071380.0217 (A34494)	Serial No. To Be Assigned
	Applicant Franke	
	Filing Date August 10, 2001	Group To Be Assigned

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09/92/861  
08/10/01

### U.S. PATENT DOCUMENTS

*Exam. Init.										Document No.	Date	Name	Class	Subclass	Filing Date if Appro.

### FOREIGN PATENT DOCUMENTS

											Document No.	Date	Country	Class	Subclass	Translation No

### OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

			KOSIM, A Multi-Level Simulator for the Design of Integrated Circuits; H. Reichl, Micro System Technologies 90, pp. 85-90, Springer, Berlin 1990.
			Einführung in die Mikrosystemtechnik, Grundlagen und Praxisbeispiele; F. Volklein. Th. Zetterer, Vieweg-Verlag, Seiten 292-326.
			Behavioral Modeling of Complex Heterogeneous Microsystems; P. Schwarz, J. Haase, S. pp. 53-62.

Examiner

Date Considered

\* Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<b>Form PTO-1449 U.S. Department of Commerce (REV. 2-82) Patent and Trademark Office</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)	Atty. Docket No. 071380.0217 (A34494)	Serial No. To Be Assigned
	Applicant Franke	
	Filing Date August 10, 2001	Group To Be Assigned

			Thermal Modelling of Semiconductor Packages, T. Franke, U. Froehler, S. pp. 193-198.
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Examiner	Date Considered
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\* Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.